

FIG. 2

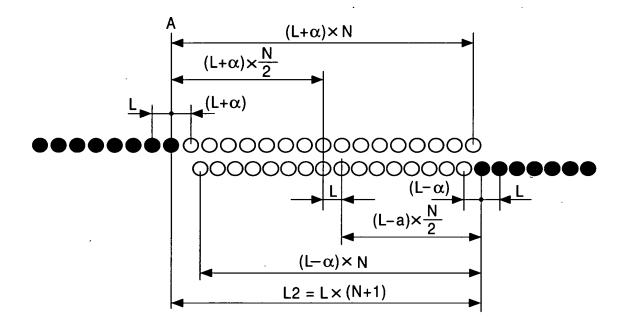
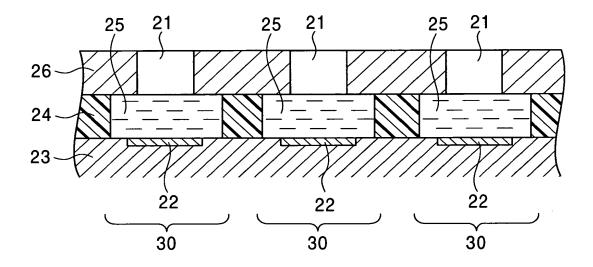
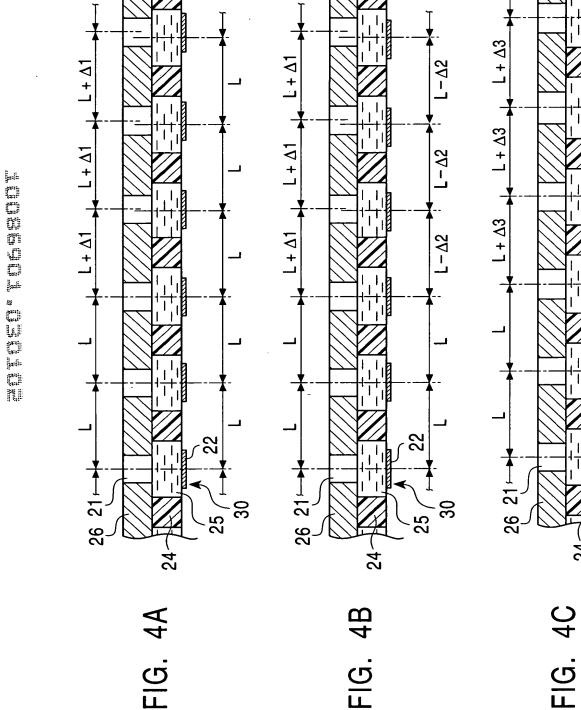
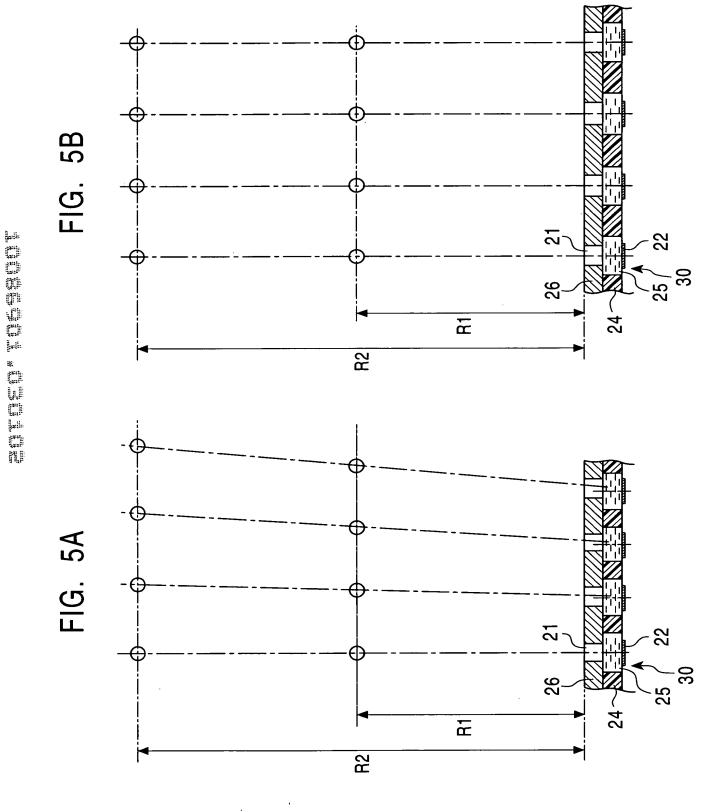


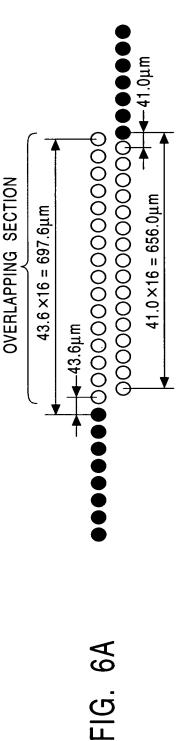
FIG. 3



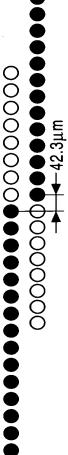


L + A3 L + A3 L + A3 30





RELATIVE DEFFERENCE IN LANDING POSITION BETWEEN CHIPS: 0µm FIG. 6B



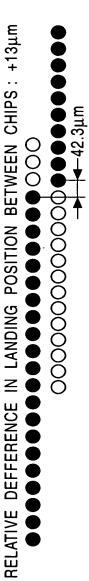


FIG. 6C

RELATIVE DEFFERENCE IN LANDING POSITION BETWEEN CHIPS: -8µm

FIG. 6D

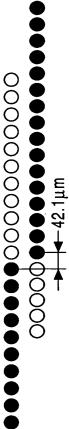
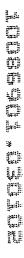
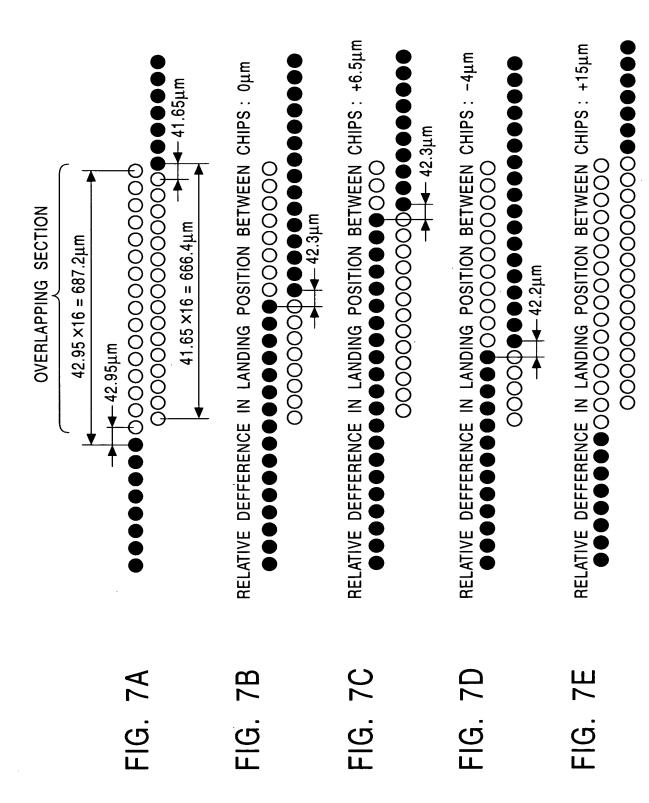


FIG. 6E

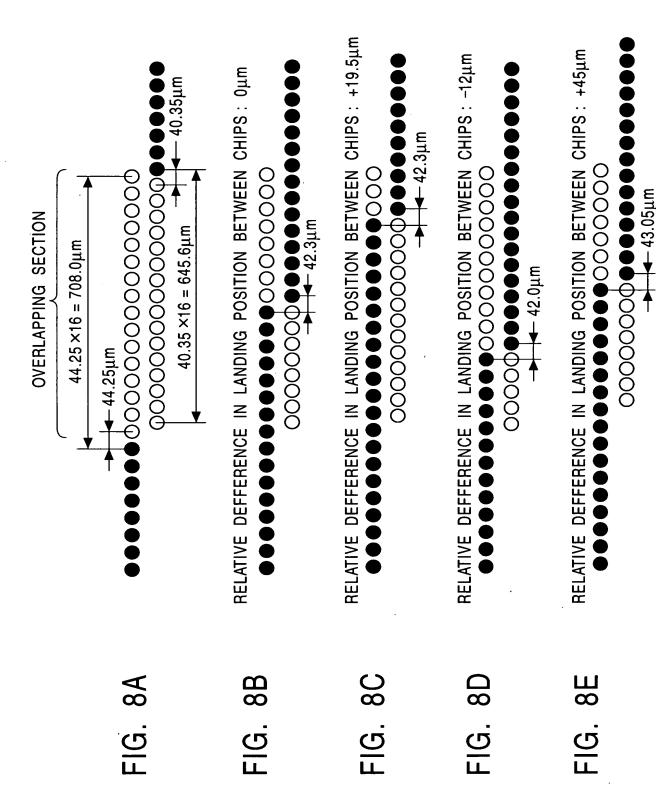












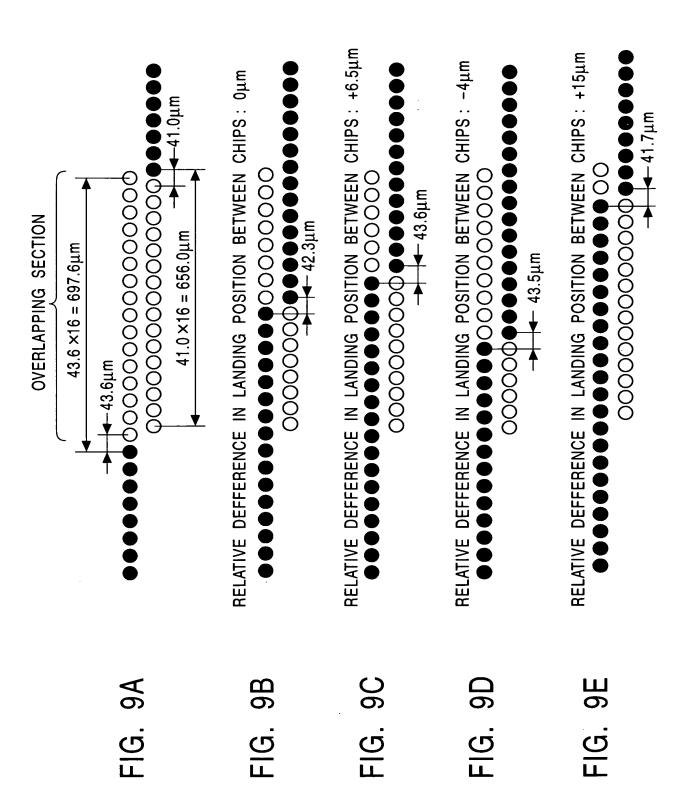


FIG. 10A

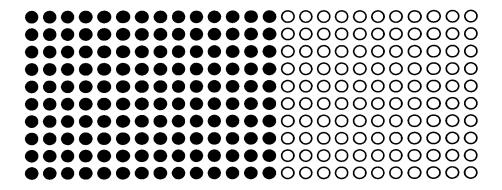


FIG. 10B

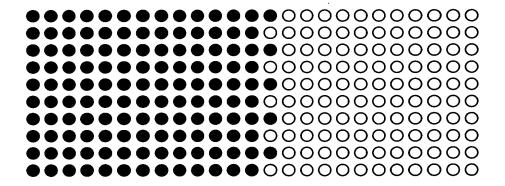


FIG. 10C

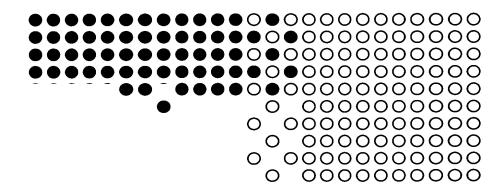


FIG. 11

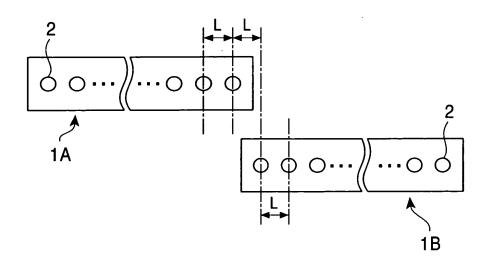


FIG. 12A

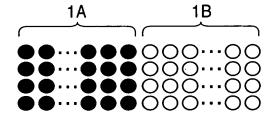


FIG. 12B

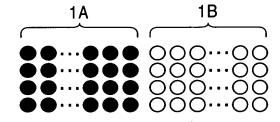


FIG. 12C

